

PUBLICATIONS IN PEER-REVIEWED INTERNATIONAL JOURNALS

1. **Vishal Kumar Gaur**, Saikat Chakrabarti, Ankush Sharma, “Improving faulty phase selection in PV integrated system with compensation angle approach,” *Electric Power Systems Research*, vol. 226, 109913, 2024.
2. **Vishal Kumar Gaur**, Bhavesh R. Bhalja, and A. Saber “New fault location method for three-terminal transmission line using unsynchronized current measurements,” *International Journal of Electrical Power & Energy Systems (EPES)*, vol. 135, pp. 1-10, 2022.
3. **Vishal Kumar Gaur**, Bhavesh R. Bhalja, and Mladen Kezunovic “Novel fault distance estimation method for three-terminal transmission line,” *IEEE Transactions on Power Delivery*, vol. 36, no. 1, pp. 406-417, 2021.
4. **Vishal Kumar Gaur**, Bhavesh R. Bhalja, “New fault detection and localisation technique for double-circuit three-terminal transmission line,” *IET Generation Transmission & Distribution*, vol. 12, no. 8, pp. 1687-1696, 2018.
5. **Vishal Kumar Gaur**, Bhavesh R. Bhalja, “A new faulty section identification and fault localization technique for three-terminal transmission line,” *International Journal of Electrical Power & Energy Systems (EPES)*, vol. 93, pp. 216-227, 2017.

PUBLICATIONS IN INTERNATIONAL/RENOWNED CONFERENCES

6. **Vishal Kumar Gaur** and Bhavesh R. Bhalja, “Wavelet transform modulus maxima- based fault location method for three-terminal transmission line,” *International Conference on Power Systems (ICPS)*, 20-22 December 2019, Jaipur, India.
7. **Vishal Kumar Gaur** and Bhavesh R. Bhalja, “Synchrophasor based fault distance estimation method for tapped transmission line,” *IEEE Conference on Smart Grid Synchronized Measurements and Analytics (SGSMA)*, 25-27 May 2019, TX, USA, pp. 1-5.
8. **Vishal Kumar Gaur** and Bhavesh R. Bhalja, “A new digital distance relaying scheme for three terminal transmission line,” *IEEE Power & Energy Society General Meeting (PESGM)*, 5-9 August 2018, Portland, USA, pp. 1-5.
9. **Vishal Kumar Gaur** and H. R. Jariwala, “Approach to solve synthetic test circuit for testing of high voltage circuit breaker,” *IEEE International Conference on Environment and Electrical Engineering (EEEIC)*, Rome, 10-13 June 2015, Italy, pp. 1187-1192.
10. **Vishal Kumar Gaur** and H. R. Jariwala, “Design and modeling of synthetic test circuit to generate four parameter TRV envelope,” *IEEE Power India International Conference (PIICON)*, 5-7 December 2014, Delhi, India.

ARTICLE IN MAGAZINE

11. **Vishal Kumar Gaur** and Bhavesh R. Bhalja, “Energy conservation,” *Electrical India Magazine*, Sept., 2015.

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12. **Vishal Kumar Gaur** and S. Paul, "Communication and Measurement Technologies for Smart Grid," in *Big Data Analytics Framework for Smart Grids*, 1st ed. CRC Press, 2023, pp. 30. DOI: 10.1201/9781032665399
13. **Vishal Kumar Gaur** and Krishna Murari, "Switchgear Faults and Remedies," in *Switchgear Design, Operation, and Maintenance Using Industry Standards*, 1st ed., Elsevier (**in press**).